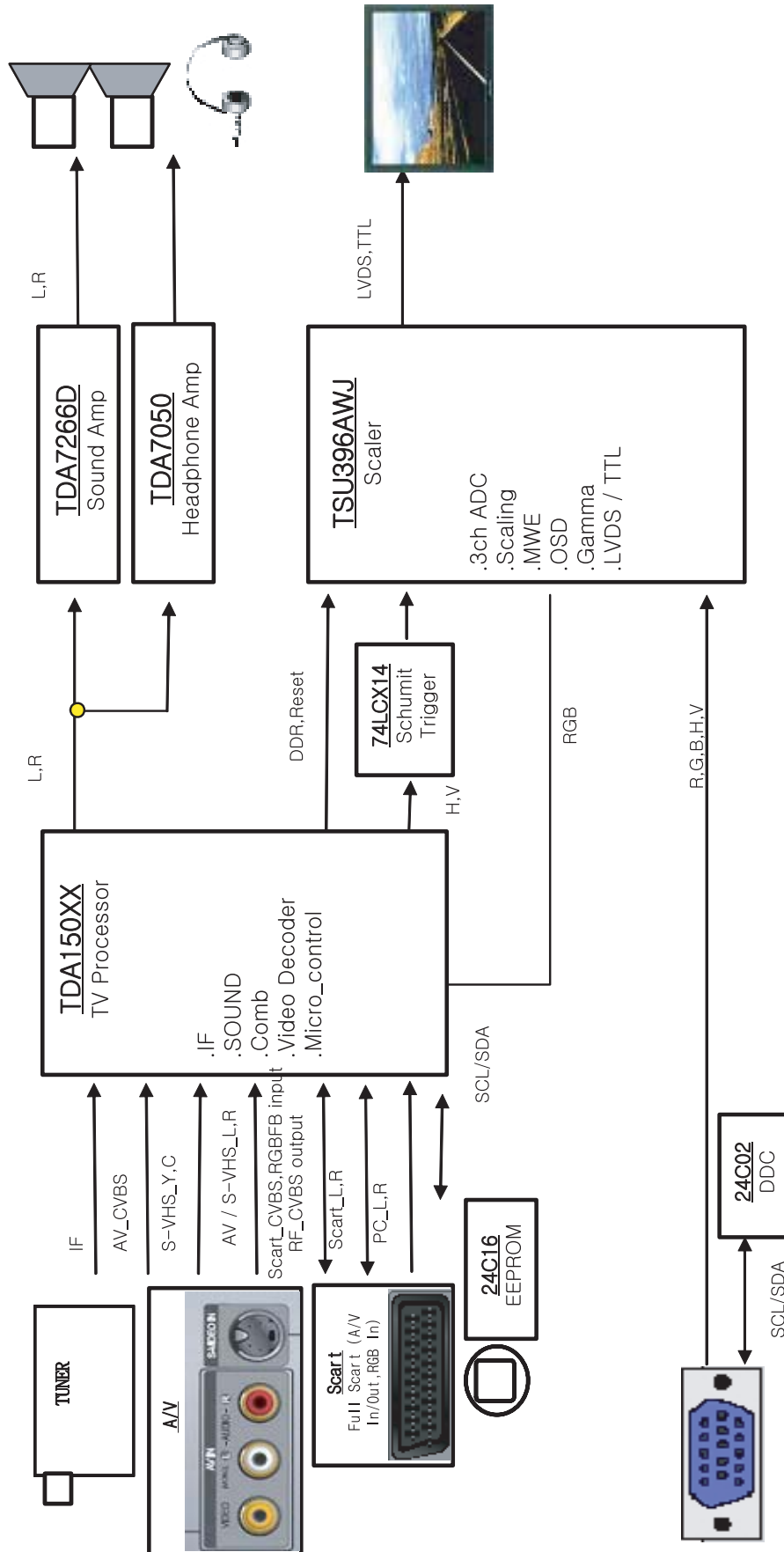


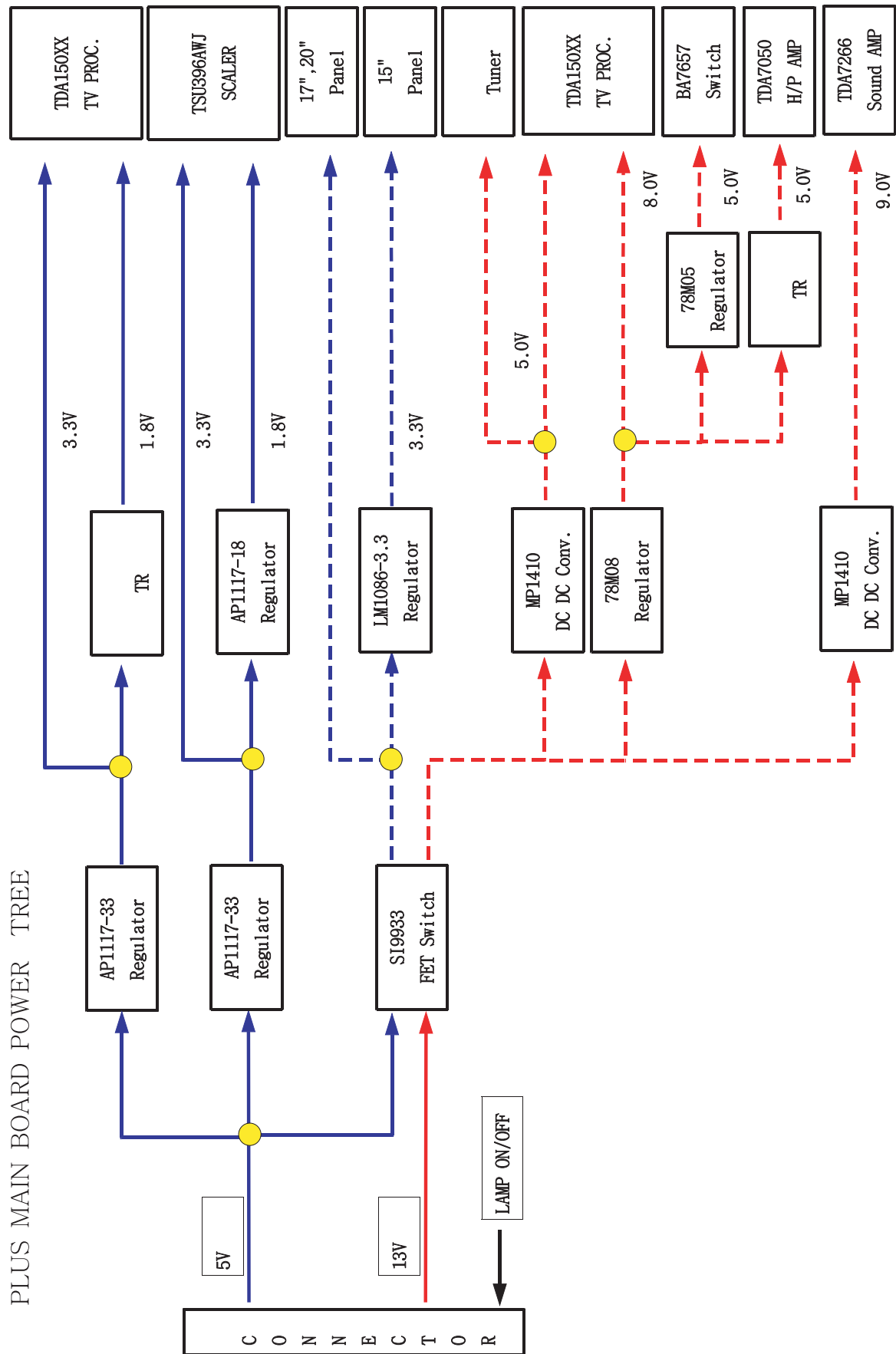
# 13 Circuit Descriptions

## 13-1 Overall Block Structure

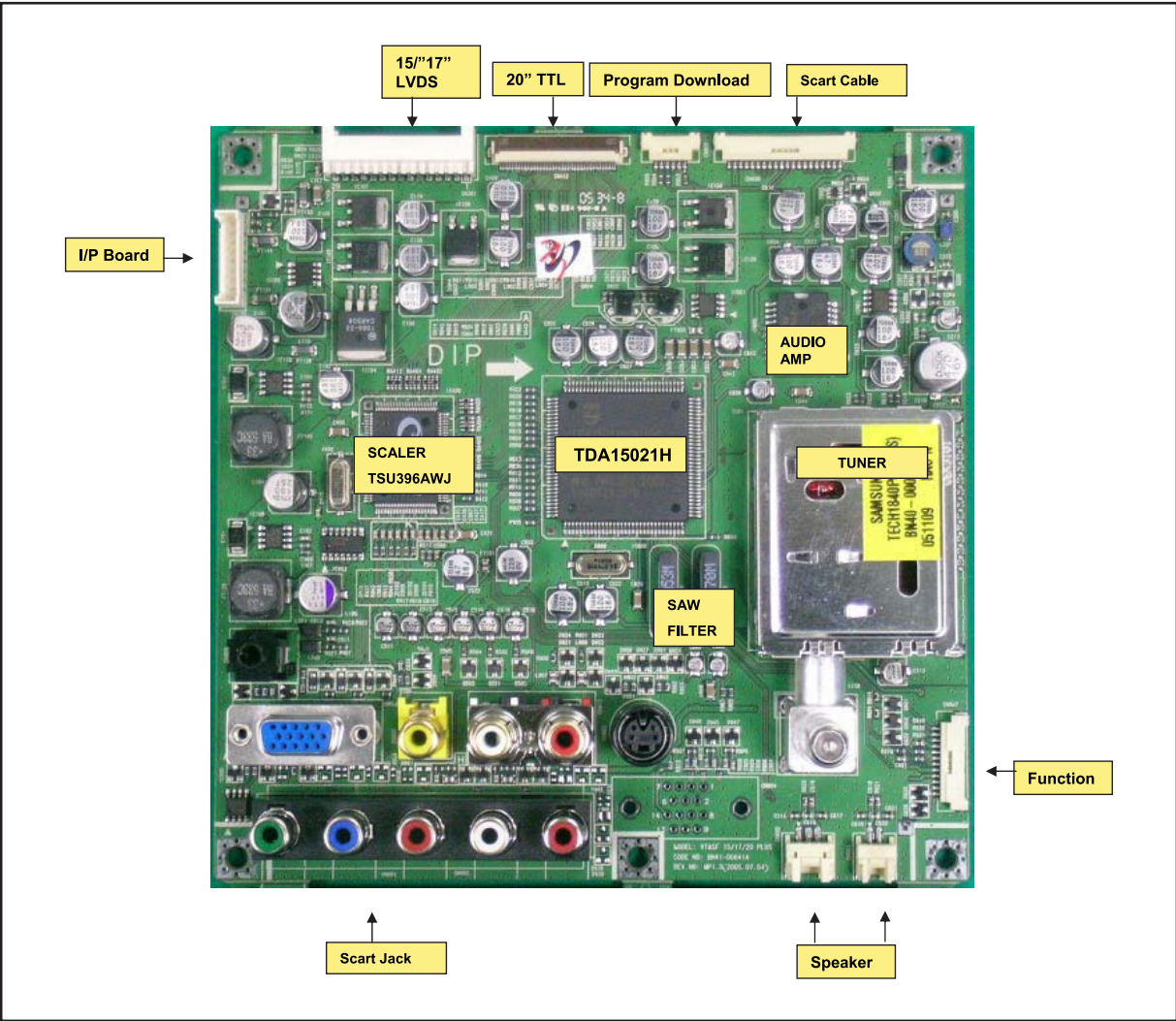


13-2 Partial Block Description

13-2-1 MAIN BOARD POWER TREE



### 13-2-2 WIRING DIAGRAM



- CN100(9P) : To IP Board
- CN401(30P) : To 15",17" Panel
- CN402(50P) : To 20" Panel
- CN905(5P) : To Program Download Jig
- CN906(20P) : To Scart
- CN600(3P) : To Left Speaker
- CN601(2P) : To Right Speaker
- CN907(12P) : To Function Board

## 13 Circuit Descriptions

**CN401**  
15",17" Panel LVDS Interface

Pin No.	AD B'D	PANEL	Pin No.	AD B'D	PANEL
1	N.C	5V	16	E0-	E1-
2	GND	5V	17	O3+	GND
3	E3+	5V	18	O3-	E0+
4	E3-	NC	19	OCK+	E0-
5	GND	NC	20	OCK-	O3+
6	ECK+	NC	21	GND	O3-
7	ECK-	GND	22	O2+	OC+
8	GND	E3+	23	O2-	OC-
9	E2+	E3-	24	O1+	GND
10	E2-	EC+	25	O1-	O2+
11	GND	EC-	26	O0+	O2-
12	E1+	E2+	27	O0-	O1+
13	E1-	E2-	28	+5V	O1-
14	GND	GND	29	+5V	O0+
15	E0+	E1+	30	+5V	O0-

**CN402**  
20 Inch Panel TTL Interface

Pin No.	Main B'D	PANEL	Pin No.	Main B'D	PANEL
1	NC	NC	26	R1	R0
2	NC	NC	27	R2	GND
3	GND	NC	28	R3	G7
4	GND	GND	29	GND	G6
5	B0	GND	30	R4	G5
6	B1	5.0V	31	R5	G4
7	B2	5.0V	32	R6	GND
8	B3	5.0V	33	R7	G3
9	GND	5.0V	34	GND	G2
10	B4	GND	35	DCLK	G1
11	B5	HSYNC/NC	36	GND	G0
12	B6	VSYNC/NC	37	DE	GND
13	B7	GND	38	GND	B7
14	GND	DE	39	VSYNC/NC	B6
15	G0	GND	40	HSYNC/NC	B5
16	G1	DCLK	41	GND	B4
17	G2	GND	42	5.0V	GND
18	G3	R7	43	5.0V	B3
19	GND	R6	44	5.0V	B2
20	G4	R5	45	5.0V	B1
21	G5	R4	46	GND	B0
22	G6	GND	47	GND	GND
23	G7	R3	48	NC	GND
24	GND	R2	49	NC	NC
25	R0	R1	50	NC	NC

**CN100**  
Inverter Power Interface

Pin No.	Main B'D	IP B'D
1	+13V	BL_EN
2	+13V	BL_BRT
3	GND	+5V
4	GND	+5V
5	GND	GND
6	+5V	GND
7	+5V	GND
8	BL_BRT	+13V
9	BL_EN	+13V

**CN907**  
Function Board Interface

Pin No.	Main B'D	Function
1	KEY2	LED_GRN
2	KEY1	LED_RED
3	GND	NC
4	HP_R	3.3V
5	HP_L	IR
6	HP_IDENT	GND
7	GND	HP_IDENT
8	IR	HP_L
9	3.3V	HP_R
10	NC	GND
11	LED_RED	KEY1
12	LED_GRN	KEY2